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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Lulz A. BARROSO et al.	§	Confirmation No.:	2813
Serial No.:	10/698,130	§	Group Art Unit:	2187
Filed:	10/31/2003	§	Examiner:	Than Vinh Nguyen
For:	Multiprocessor Cache Coherence System And Method In Which Processor Nodes And Input/Output Nodes Are Equal Participants	§ § § § § § §	Docket No.:	200302253-2

RESPONSE TO OFFICE ACTION DATED SEPTEMBER 23, 2004

Mail Stop Amendment
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Date: December 14, 2004

AMENDMENT

Sir:

In response to the Office action of September 23, 2004, please amend the above-identified application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims that begins on page 3 of this paper.

Remarks/Arguments begin on page 6 of this paper.